

**SIES COLLEGE OF ARTS, SCIENCE AND COMMERCE,
AUTONOMOUS, Sion w.**

Workshop Report

23.04.2022

Title: Designing and testing of Power supply and Applications of Logic gates.

Background:

Due to two years of pandemic, students could not have opportunity to work in laboratory. Therefore to provide some experience and joy of working in physics laboratory, two day workshop was organized.

Aims/ Objectives of the program:

1. To provide basic knowledge of electronic components.
2. To Design and construct a power supply.
3. To study different applications of Logic gates.

Location: Offline lectures and Demo-Sessions in SIES College Physics Laboratory.

Target Audience /Participants with expected number:

SYBSc students: Expected students: 29 (registered through Google form)

Details of Session:

Students from SYBSc attended the workshop.

The program was conducted in Physics lab consisting of 2 hour lectures from 10.00 am to 12.00 pm followed by practical from 12.30pm to 2.30pm.

Sr. No	Date	Lecture Title	No. of Lecture/Session
1.	20-04-2022	Designing of power supply	An offline lecture of 2 hours followed by 2 hours of lab-work
2.	21-04-2022	Logic gates.	An offline lecture of 1 hour followed by 2 hours of lab-work

Name of the Resource Person:

1. Vinod Menon
2. Kiran Nabar
3. Aarti Muley
4. Anand Ambardekar
5. Vishal Dev Ashok
6. Maheshwar B. Falake
7. Seema Jadhav
8. Naziya shaikh sultana
9. Mohammed Jeelani
10. Vasuthevan Thyagarajan
11. Vishal Desai
12. Bhavya Shetty

18 students attended lectures and participated in practicals (lab-work).

The feedback of participants reflects the following points:

- The program helped them to understand Power supply and Applications of logic gates.
- They enjoyed working in laboratory after a long gap.
- More such activities are awaited.
- Not preferring online sessions.

Convener:

Maheshwar Falake

Maheshwar Falake

Vishal Dev A.

Vishal Dev A.